



29th Euromicro Conference on Digital System Design – Call for Papers

AGH University of Krakow, Poland
02 – 04 September, 2026



Scope

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system development and covers the entire design process from specification to microarchitectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced research, development and applications. It focuses on the current and future challenges of: advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high-performance HW/SW systems; design methodology and design automation for all design levels of embedded, high performance and cyber-physical systems; modern implementation technologies ranging from full-custom circuits in nanometer technology nodes to FPGAs and MPSoC infrastructures. Authors are cordially invited to submit their work on (but not limited to) the topics of the main track of the conference. In addition, contributions to various special sessions (with dedicated coordinators and sub-program committee) are welcome. All papers will be reviewed according to common guidelines, quality requirements, and acceptance thresholds.

Topics

Papers on any of the following and related topics can be submitted to DSD main track:

- **Artificial intelligence from edge to cloud: architectures, methods, tools and applications.**
- Design and synthesis of systems, hardware and embedded software – specification, modelling, analysis, validation and testing.
- Design automation at system, processor, register-transfer, logic and physical levels.
- Formal methods in system and hardware design.
- IoT, cyber-physical, embedded systems and applications.
- Systems-on-a-chip, networks-on-a-chip and systems-in-a-package.
- High-performance, energy-efficient multi-core and many-core (heterogeneous) processor architectures.
- Autonomous/adaptable/reconfigurable systems and architectures.
- Security, safety, reliability and multi-objective optimization of embedded and cyber-physical systems.
- Future trends, new applications and new technologies.

DSD Special Sessions

AHSA	Architectures and Hardware for Security Applications
ASHWPA	Advanced Systems for Health Wellness and Personal Monitoring
DDVC	Digital Design and Verification with Chisel
DTFT	Dependability, Testing and Fault Tolerance in Digital Systems
EMPDSD	European and Major Multi-Partner Projects in Digital Systems Design
HSTIEC	Hardware, Software, and Tools for the IoT-to-Edge-to-Cloud Continuum
MATTERV	Opensource Methods, architectures, tools and technologies for RISC-V
SDSD	Sustainable Digital System Design
SPCPS	Safety, Security and Privacy of Cyber-Physical Systems

P. Kitsos, M. Novotny, A. Papadimitriou
F. Loporati, R. Stojanovic, E. Marenzi
M. Schoeberl
P. Fiser, H. Kubátova
L. Jozwiak, F. Loporati, E. Torti
F. Aromolo, G. Rossolini, D. Casini
S. Pillement, K. Martin, L. Zarouar
T. Scheipel, M. Baunach, L. Zarouar
J. Sepúlveda, S. Dey, S. Park

Submission Guidelines

Authors are required to submit their manuscripts via EasyChair <https://easychair.org/conferences/?conf=dsd2026>.

Important Dates

Submission: 11.05.2026

Acceptance: 29.06.2026

Camera ready: 06.07.2026

Journal Publication

Authors of the selected papers will be invited to submit extended versions to the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) which has a 2025 Impact Factor of 2.6 and Cite Score of 8.2 (Q1).



<https://dsd-seaa.com>





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- **Marek Gorgon** (AGH University of Krakow, PL), email: mago@agh.edu.pl

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- **Karl-Erwin Grosspietsch** (Euromicro, DE)

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- **Hubert Szolc** (AGH University of Krakow, PL)

Publicity Chair

- **João Canas Ferreira** (University of Porto, PT)

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- Eugenio Villar (University of Cantabria, ES)

DSD Technical Program Committee

To be announced