

Wednesday						
SCHEDULE						
Welcome Session (Organizers, PC Chairs) - Room: Sala Vietri						
Keynote 1: Matteo Andreozzi, ARMA, "Architecting the Future: Scalable QoS and Predictability with MP4M in Heterogeneous Systems" - Room: Sala Vietri - Session Chair: Francesco Cazorla						
Coffee Break - Foyer Cona						
Time	SEAA Track 1	SEAA Track 2	SEAA Track 3	DSB Track 1 (Room: Sala Vietri)	DSB Track 2	DSB Track 3
08:30 - 09:00						
09:00 - 10:30						
10:30 - 11:00						
11:00 - 12:30	Session: DAIDE 1 Session chair: Helena H. Olsson and Jan Bosch Room: Sala Maiori (11:00 - 11:15) VISION: Vladislav Indjov, Daniel Strüber and Rebekka Wöhrich, MLTrackCops: Embedding Trade-Off Management into the ML-Cops Workflow	Session: KXIO 1 Session chair: Andrzej Zalewski Room: Sala Ischia (11:00 - 11:15) FULL: Michael Neumann, Tom Strzeliński, Alex Schwab, Daniel Fülth, Quoc Trung Vu, Maurice Lotze, Joshua Borgmann, Justus Donner, Melanie Horstmann, Tobias Knapik, Gino Ismaili, Lars Wesemann, Tim Meierlens, Eze-Maria Schön, Lars Baumann and Julia Spanke, "Give Software Developers Time": Investigating Perceived Performance and Stress in a Compressed Work Schedule	Session: SM1 Session chair: Roberto Recupito Room: Sala Furio (11:00 - 11:15) Rodrigo Soltero, Savio N. S. Santos and Rodrigo Sampaio, Exploring Software Fairness Debt in IoT Literature	Session DSD 1-1: Digital Design and Verification with Chisel (DDVC) Session Chair: Marcela Zachariakova; Room: Sala Maiori (11:00 - 11:15) Robin Selten, Christian Ewert, Rainer Buchty, Maden Berevoic and Saleh Muhem, Nail: Not Another Fault Injection Framework for Chisel-generated RTL	Session DSD 1-2: Dependability, Testing and Fault Tolerance in Digital Systems (DTFT) Session Chair: Hana Kubatova Room: Sala Maiori (11:00 - 11:15) Federico Bucchelli, Corrado De Ro, Sarah Ajami and Luca Sterpone, On Hardware Resilience Analysis of DPU-Accelerated CHNs on FPGA-Based Systems	Session DSD 1-3: AI Acceleration and Automation for Embedded Systems Session Chair: Luigi De Simone Room: Sala Postano (11:00 - 11:15) Carolina Gallardo-Pavesi, Javier E. Soto, Yaimé Fernández, Cecilia Hernández and Miguel Fuentes, A streamlining algorithm and hardware accelerator for top-K flow detection in network traffic
12:30 - 14:00	(12:15 - 12:30) VISION: Muteen Ahmed Abbas, Petri Ilamäki, Tommi Mikkonen and Niko Mäkelä, Reconsidering Requirements Engineering: Human-AI Collaboration in AI-Native Software Development (12:30 - 12:45) FULL: Silvio Alonso, Antonio Pedro Santos Alms, Lucas Romão, Helio Lopes and Marcos Kalinowski, Define-ML: An Approach to Idealize Machine Learning-Enabled Systems	(12:15 - 12:30) FULL: Jonathan Madsen, Tim Bauer, Florian Eick, Michael Neumann, Julia Spanke, Eze-Maria Schön and Adam Prybyłek, Unlinking the ODK framework: Insights into Benefits and Barriers for an Effective Implementation in Practice (12:30 - 12:45) FULL: Ahsan Dabbak, Piero Daniele, Caroline Muehlbauer and Fredrik Rydgberg, What makes customers reluctant to deploy frequent software releases? A case study from the telecommunications domain	(12:15 - 12:30) Emanuele Giustini, Mauro Caporiccio and Diego Perez-Palacin, A Reasoning Framework for Architecting Carbon-aware Software-as-a-Service Applications (12:30 - 12:45) Giammaria Giordano, Antonio Della Porta, Flaminia Ferrucci and Fabio Palomba, An Evidence-based Study on the Relationship of Software Engineering Practices on Smells in Python ML Projects	(12:15 - 12:30) Tjark Petersen, Luca Pezzarossa and Martin Schoeberl, A Structured Approach to Verification of Digital Hardware in Scala (12:30 - 12:45) Yi Yang, Paul Delacretaz and Ahmed Hemani, Modeling and Scheduling of Composable Instruction Set	(12:15 - 12:30) Nicola Bellamini, Riccardo Cantoni, Martin Huch, Tobias Kilian and Giovanni Squitieri, In-Context Learning for Microcontroller Performance Screening Using Tabular Foundation Models (12:30 - 12:45) Martha Schmeier and Ralf Drechsler, Synthesis for Testability: Polynomial Test Pattern Generation for KFD0 Circuits	(12:15 - 12:30) Karish R. Sai, Vishwanath Ruchi Komaragiri, Yash Sengupta and Mahav Rao, VPBA: A Vectorized Processing Element configured Synthetic Array Architecture Generator (12:30 - 12:45) Rodrigo Omos, Sergio Hernandez, Pedro Lobo, Eduardo Casanueva and Andres Olivo, Hardware-aware Decision Tree Ensembles for Radar Data Processing on the Edge with Online Learning (Outstanding Paper Award)
14:00 - 15:30	(12:45 - 12:50) FULL: Erik Eriksson, Joel Gustavsson, Vladislav Indjov, Daniel Strüber and Rebekka Wöhrich, Extracting Design Patterns from Mixed Component Models of ML-Enabled Systems (12:50 - 13:05) FULL: Matthias Preuner, Paul Grinbaches, Pedro Luis de Paula Filho and Alexander Eyerd, Feature-based Versioning for ML-Enabled Product Lines	(12:45 - 12:50) SHORT: Mikail Akbulut Sami, Zheyang Zhang, Muhammad Waseem, Kai-Kristian Kemmler, Zeshan Rashid, Tomas Herda, Md Toufique Hasan, Justus Rasku and Pekka Abrahamsson, A Multi-Agent LLM System for Automated Requirements Analysis: A Study on User Story Generation and Prioritization (12:50 - 13:05) SHORT: Adam Roman, Michal Michniewicz and Jaroslaw Wroblewski, "To Measure is to Know", but Not in Software Engineering: A Call for Operational Definitions of Code Metrics	(12:45 - 12:50) Simon Sun, David Friberg and Mikaela Stano, "Good" and "Bad" Failure in Industrial C/CD - Balancing Cost and Quality Assurance (12:50 - 13:05) Vasilios Mavroulis, Konstantinos Georgios, Nikolaos Mitsas and Lefteris Angelis, H-TUNE: Detecting optimal Green Software Engineering Skills using TUNE Analysis and Hierarchical Cumulative Voting	(12:45 - 12:50) Arash Sadat Sadatmand, Todor Stefanov, Andy D. Prentiss, Benny Aleson and Ignacio Gonzalez-Alos, Unravelling Parallelism in Automated Workload Assessment for Distributed Cyber-Physical Systems (12:50 - 13:05) WIP 7 min - Vignesh Marimuthu, Tobias Scheipel, Jesus Petzana and Marcel Baunach, Queryable Microarchitecture Knowledge Base using Retrieval-Augmented Generation	(12:45 - 12:50) Louis Ledoux, Design-Space Exploration of Serialized Floating-Point Division for DLP Architectures (12:50 - 13:05) Han Yu, Sigrid Eick, Kristian Wiklund, Andreas Emeisahl, Philipp Haler and Cyrille Artho, Auto-large without test cases: How LLMs fix compilation errors in real industrial embedded code	(12:45 - 12:50) Hsin-Yu Ting, Sing-Yao Wu, Leming Cheng and Eli Buzurgshahi, Dynamic Multi-Accelerator Management for Deep Learning Applications on the FPGA Edge (12:50 - 13:05) WIP 7 min - Jian-Yu Wang, Leming Cheng and Eli Buzurgshahi, Dynamic Multi-Accelerator Management for Deep Learning Applications on the FPGA Edge
15:30 - 16:00						
16:00 - 17:30	Session: DAIDE 2 Session chair: Helena H. Olsson and Jan Bosch Room: Sala Vietri (16:00 - 16:15) FULL: Mohammad Mehdi Akhavan, Iman Hamed Moghadam, Vadim Zaytsev, Mohammad Hossein Akhavan and Hossein Baharvand, Refactoring Detection Across Languages: Leveraging Java-Translated Models for Detecting Class-Level Refactorings in Kotlin	Session: KXIO 2 Session chair: Andrzej Zalewski Room: Sala Ischia (16:00 - 16:15) FULL: Md Toufique Hasan, Muhammad Waseem, Kai-Kristian Kemmler, Ayman Asad Khan, Mika Saari and Pekka Abrahamsson, Engineering RAG Systems for Real-World Applications: Design, Development, and Evaluation	Session: SM2 Session chair: Omar Deminors Room: Sala Furio (16:00 - 16:15) Fabio Palomba, Giammaria Viora, Alessandra Parziale, Viviana Pertanagola, Antonio Della Porta, Vincenzo Ferrucci, Giammaria Giordano and Giammaria Giordano, Teaching Software Engineering for Artificial Intelligence: An Experience Report	Session DSD 2-1: European and Major Multi-Partner Projects in Digital Systems Design (EMPSD2-1) Session Chair: Lech Jozwiak Room: Sala Maiori (16:00 - 16:15) Johan Pamp, Folke van Meulen, Jan José Lopez Escobar, Eli De Poorter, Jeron Heidecker, Geert Vanthienen, Michiel Rolske, Roberto Priests, Raul Santos de la Cámara, Luca David and Jacomijn Hubalen, DiamMSM: Distributed Multi-Sensor Systems for Human Safety and Health	Session DSD 2-2: Architecture and Hardware for Security Applications (AHSA)1 Session Chair: Paris Kikios Room: Sala Maiori (16:00 - 16:15) Vil Masek, Vojtech Mikovsky and Matúš Oleksák, Building a Side-Channel Attack Scheme on Siphix FPGA Implementation	Session DSD 2-3: Hardware, Software, and Tools for the IoT-to-Edge-to-Cloud Continuum (HSTEC) Session Chair: Alessandro Biondi Room: Sala Postano (16:00 - 16:15) Marco Barletta, Marcello Cinque, Luigi De Simone and Stefano Torsocani, PREEMPT-KES: Pod Prioritization for Mixed-Criticality Edge-Cloud Services
17:30 - 19:00	(16:15 - 16:30) FULL: Vasilios Christos Moudounis, Karan-Darshit Patra and Simona Montagna, LLMs-based Data Augmentation Techniques for Python Code Refactoring Classification (16:30 - 16:45) FULL: Marius Krenetzer, Maximilian Kirschner and Jürgen Becker, Towards a Service-Oriented Infrastructure for Distributed Systems with Heterogeneous AI Accelerators	(16:15 - 16:30) FULL: Dimitrios Christos Moudounis, Karan-Darshit Patra and Simona Montagna, LLMs-based Data Augmentation Techniques for Python Code Refactoring Classification (16:30 - 16:45) FULL: Sadhana Lakshminarayanan and Romania Szelezak, Intelligent Assembly Detection in Manufacturing: The Kitchen Cabinets Industrial Case	(16:15 - 16:30) Yashu Elouadi, Selim Ural, Umur Chak, Hakan Erdogmus and Enay Tuzlin, DevChisel: A Review-Based Approach for Evaluating Developer Contributions (16:30 - 16:45) Marco De Luca, Sergio Di Martino, Sergio Di Meglio, Anna Rita Fasolino, Luigi Libero Lucio Starace and Perfino Tronci, Rocker Misakes: Measuring Software Quality in Student Projects to Guide Educational Enhancement	(16:15 - 16:30) Anders Lindgren, Distributed AI Systems at Scale: Reflections from DAIS (EMPSD2-1) (16:30 - 16:45) Martin Kaiser, Jens Hagenweyer, Lennart Tiggens, Christian Klarhorst, Björn Wolf, Fred Buring, Bodo Fahrenberg, János Ladányi, René Griesel, Muhammad Shahzad, Yannis Georgios, Salim Mounir, Pedro Velho, Michael Menzer, Eva Trunger, Julian Gajewski, Stefan Knupp, Michi Vor dem Berge, Desapik M. Mathew, Skips Dimitrios, Amrinda Indrath, Orestis Vassiliou, David Georgantzas, Gaurav Rousake, Christoph Butler, Guato Shvachenski, Brandon Lewis and Angela Heuber, CAPE - European Open Compute Architecture for Powerful Edge	(16:15 - 16:30) Eberhard Boehr and Guenter Heggen, Artificial Intelligence Enables Increased Data Rate of True Random Number Generator (16:30 - 16:45) João Carlos Resende and Ricardo Chaves, AEGIS+AES folded architecture for FPGA (Outstanding Paper Award)	(16:15 - 16:30) Mario Bartareschi, Salvatore Barone, Niccolò Mazzocco and Alberto Moriconi, Designing Energy-Efficient Approximate Circuits for the FPGA Technology (16:30 - 16:45) Jeremy Jens Giesen Leijn, Isak Ingvald Cabero, Enrico Mezzetti, Jaume Abella Ferrer and Francisco J. Cazorla, Impact of Content-Aware Placement in Heterogeneous Edge Devices
19:00						
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19:30 - 20:00						
20:00 - 21:30	Session: SMSE 1 Session chair: Danilo Di Nucci Room: Sala Maiori (17:00 - 17:15) FULL: Evangelos Ntontos, Francesco Uboldi and Uwe Zdm, ML Pipeline Insights Service for Rule-Based Assessment of Training Practices in Reinforcement Learning	Session: KXIO 3 Session chair: Andrzej Zalewski Room: Sala Ischia (17:00 - 17:15) FULL: Bruno Monteiro, Giammaria Giordano and Nuno Portilo, Unravelling Out Code Smells: A Multipass Framework for Accurate and Scalable Detection	Session: SM3 Session chair: Luigi Lucio Libero Starace Room: Sala Furio (17:00 - 17:15) Sergio Di Meglio, Valeria Pontillo and Luigi Libero Lucio Starace, REST in Pieces: Code Quality Issues and RESTful Design Rule Violations in Student-Built Web Apps	Session DSD 3-1: Special event on Non-Functional properties - Design exploration: energy, power, reliability Session Chair: Lech Jozwiak Room: Sala Maiori (17:00 - 17:15) Matteo Sonza Reorda, Pollicino di Torino, Overall architecture and organization of the project	Session DSD 3-2: Opensource Methods, architectures, tools and technologies for RISCV (MAIT-2024) Session Chair: Sébastien Pillement Room: Sala Maiori (17:00 - 17:15) Guilherme Vainero de Oliveira, Vinícius Prassol, Luis Sousa and Nuno Palavras, RISCV+ Towards an HLS-Defined RISCV SoC	Session DSD 3-3: Sustainable Digital System Design (SDSD) and Hyperspectral Imaging Applications, Algorithms and Architectures Session Chair: Marcel Baunach and Tobias Scheipel Room: Sala Postano (17:00 - 17:15) Ann M and Mahav Rao, Efficient POSIT Multiplexer with Multi-Priority Encoding and Multistage Buffer Processing
21:30 - 23:00	(17:15 - 17:30) SHORT: Robert Heumüller, Theo Langer and Frank Ottensmeyer, Identification of OpenAI Embeddings for Semantic Code Review Comment Similarity	(17:15 - 17:30) FULL: Ewa Wodzyńska, Jarosław Heumüller and Adam Roman, Leveraging Large Language Models for Software-Centric Detection	(17:15 - 17:30) FULL: Sergio Di Meglio, Valeria Pontillo and Luigi Libero Lucio Starace, REST in Pieces: Code Quality Issues and RESTful Design Rule Violations in Student-Built Web Apps	(17:15 - 17:30) Marco Andreoli, Roberto Ammendato, Andrea Bagnoli, Carlotta Chianini, Omberto Frezza, Francesca Lo Corno, Alessandro Lenardo, Pier-Silvestro Paolucci, Elena Pastorelli, Pierpaolo Perinacci, Luca Pontisso, Cristian Rossi, Francesco Simola, Piero Vigni, David Coll, Grigore Pitoh, Alexandre Louvet, John Gledberg, Claire Chen, Matteo Tulliani, Andrea Montebianchi, Jean-Philippe Nominé, Denis Dubot, Hugo Taborda, Lila Zaourar, Mohamed Benaboud, Angelika Blum, Fabian Chai, Marius Krenetzer, Nikolay Chrysoy, Evangelos Magaziotopoulos, Christos Kozanakis, Thomas Mohr, Steffen Persvold, Einar Rustad, Sander Fries, Simone Pellerin, Fabrizio Geronzi, Raffaele Pusterla, Luca Pastera, Philippe Veuha, Flavio Velis, Daniele De Senis and Salvatore Portanti, Pioneering the Future of Interconnects for Supercomputing and AI	(17:15 - 17:30) Jan Doležal, Martin Jureček and Oliver Roman, Distributed Quantized Machine Learning Inference Using Commodity SoC-FPGAs Using FINN	(17:15 - 17:30) Tobias Scheipel, Maximilian Ogier and Marcel Baunach, You Shall Not Stall: Achieving RISCV-V On-Demand Runtime-Reconfiguration using SCAL-V
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	(17:00 - 17:15) FULL: Marina Araujo, Julia Araújo, Roney Oliveira, Lucas Rundo and Marcos Kalinowski. Domain Knowledge in Requirements Engineering: A Systematic Mapping Study	(17:00 - 17:15) WIP: Shady Hegazy, Nouran Nussairah, Christoph Elsner, Jan Bosch and Helena Holmström. Optimal Graph Model Schema Injection for Large Language Model-Generated Cyber Queries	(17:00 - 17:15) SHORT: Leif Bonorden. An Empirical Analysis on the Use of Third-Party HTTP Clients in Open-Source Java Projects	(17:00 - 17:15) Giovanni Agosta, Marco Aldrucci, Andrea Bartolini, Laura Bellentani, Andrea Biagioni, Daniele Cesari, Carlotta Chiarini, Jacopo Colomwelli, Pietro Delugas, Lev Denisen, Ottorino Frezza, Francesca Lo Cicero, Alessandro Lonardo, Michele Martinelli, Andrea Masilo, Mauro Oliveri, Pierpaolo Perticaroni, Luca Pontisso, Cristian Rossi, Davide Rossi, Sergio Saponara, Marco Giannetto, Antonio Sciarappa, Francesco Simola, Matteo Sona Renda, Massimo Torquati and Piero Vicini. Towards RISC-V-based HPC: The Italian Pathfinding Activities in the DARE-SGA1 Project	(17:00 - 17:15) John Reuben and Dietmar Fey. Smart Sensing of Multi-bit Resistive Memory Using a Single Reference	(17:00 - 17:15) Petr Bardonek and Marcela Zachariášová, Leveraging Design Static Analysis for Vertical Reuse in Functional Verification
	(17:15 - 17:30) WIP: Vinícius dos Santos. On a Maturity Model for Systematic Literature Review Processes	(17:15 - 17:30) WIP: Shady Hegazy, Muhammad Ammar, Jan Bosch, Christoph Elsner and Helena Holmström. Ooson. Prompt-to-Metric LLMs and Graph Algorithms for Platform Ecosystem Health Monitoring	(17:15 - 17:30) SHORT: Alfonso Camarale, Emanuele Iannone, Gianluca Di Lillo, Fabio Palomba and Andrea De Lucia. The Ground Truth Effect: Investigating SZ2 Variants in Just-in-Time Vulnerability Prediction	(17:15 - 17:30) Akshat Jodha, Dae Jyoon, Patel Suresh, Shruti Kathuria, Josef Strmadel, Caitlin Quinn, Václav Šmek and Patrizia Staroň. Portable Simulation Models for Energy Aspects of IoT Devices in the LoPiLo-IoT Project	(17:15 - 17:22) (WIP 7 min) - Abusua Jafari, Hassan Ghazemzadeh Mohammadi and Marco Platzner. CRC: Compressed Reservoir Computing on FPGA via Joint HSC LASSO-based Pruning and Quantization	(17:15 - 17:30) Alfonso Mascareñas Gonzalez and Youcef Bouchebaba, Analysis of modern Memory Management Unit functioning for critical systems
19:30	Social Dinner - Mediterranean Hotel - Resort & Convention Center					
	Friday					
	SCHEDULE					
	Keynote 5: Mohammad Al Faruque, University of California at Irvine. "Cyber-Physical Vulnerabilities in Autonomous Systems: Facing Hard Realities and Shaping the Road Ahead"					
	Room: Sala Vietri - Session Chair: Eugenio Villar					
	Coffee Break - Foyer Conca					
	SEAA Track 1	SEAA Track 2	SEAA Track 3	DSO Track 1	DSO Track 2	DSO Track 3
11:00 - 12:30	Session SMSE 3 Session chair: Giannaria Giordano Room: Sala Maiori	Session CPS 1 Session chair: Verena Kötts Room: Sala Ischia	Session ECT 1 Session chair: Davide Tabi Room: Sala Furure	Session DSD 5-1: Advanced Systems for Health Wellness and Personal Monitoring (ASHWPA) Session Chair: Francesco Leporati Room: Sala Vietri	Session DSD 5-2: Safety, Security and Privacy of Cyber-Physical Systems (SPCPS) Session Chair: Johanna Sepúlveda Room: Sala Amalfi	Session DSD 5-3: Reconfigurable Acceleration and Real-Time Processing on FPGA Platforms Session Chair: Tomasz Kryjak Room: Sala Positano
	(11:00 - 11:15) FULL: Andrada-Mihaela-Nicoleta Moldovan. Regression Testing via Tracability: A Systematic Literature Review	(11:00 - 11:20) FULL: Ehsan Elahi, Matteo Camilli and Raffaella Miranda. Counterfactual Self-adaptation in Cyber-Physical Systems	(11:00 - 11:15) Dimosthenis Natsos and Andreas L. Symeonidis: A Defense-in-Depth Approach for Securing Collaborative Cloud Infrastructures	(11:00 - 11:15) Bens Baeyens, Dennis Laurijssen, Jan Steckel and Walter Dams, PhysioLog: Multimodal Compressive Sensing Platform for Wearable Health Monitoring	(11:00 - 11:15) Frida Sundfeldt, Bianca Widstam, Mahdi Heli Moghadam, Kuo-Yun Liang and Andreea Venterberg. Adapting Attack Data Scarcity: SCANNA's Experience Towards Enhancing In-Vehicle Cyber Security Measures	(11:00 - 11:15) Fatemeh Shrinazadeh, Abhoy Kote, Kamalika Datta, Saideeh Shrinazadeh and Rolf Drechsler. A Comprehensive Synthesis and Verification Approach for BRAM-Based Neuromorphic Computing (Outstanding Paper Award)
	(11:15 - 11:30) FULL: Felipe Gomes, Thiago Mendes, Savi Freire, Rodrigo Spinola and Manoel Mendonça. Investigating the use of Snowballing on Q&A websites	(11:20 - 11:40) FULL: Yasmin Rafiq, Grot Viquez, Radu Calinescu, Sanja Dogramaci and Robert M. Hieros. Symbolic Runtime Verification and Adaptive Decision-Making for Robot-Assisted Dressing	(11:15 - 11:30) Fabio Piesani, Aoma Taberi, Moirfaed, Andrea Bombarda and Angelo Gargantini: QuTiP-MRL: A Library for Multiple-Valued Reversible Logic Simulations	(11:15 - 11:30) Giuseppe Longo, Andrea Fasolino, Rosalia Uguroi, Luigi Di Benedetto, Gian Domenico Liccardo and Alfredo Rubino. ST-HAR: Stretchable Sensor-Only Tiny Computational Neural Network for Human Activity Recognition	(11:15 - 11:30) Dames Gruska. Adversarial Analysis and Supervisory Control of Sensor Networks	(11:15 - 11:30) Mateusz Wasala, Mateusz Smolarczyk, Michal Danilowicz and Tomasz Kryjak. Hardware-Aware Feature Extraction Quantization for Real-Time Visual Odometry on FPGA Platforms
	(11:30 - 11:45) SHORT: Osório Santos Gomes, Glauca Braga I Silva, Erica Ferreira de Souza, Luciana Rebelo, Nandamudi Lankalapalli Vijaykumar and Gabriel Zolga Fernandes. How have ethics been addressed in the software development lifecycle? A Systematic Mapping Study	(11:40 - 12:00) FULL: Qi Zhang, Ioannis Stefanakos, Javier Camara and Radu Calinescu. Robot Mission Adaptation with Quantitative Guarantees	(11:30 - 11:45) Aurora Macias, Evangelos Ntrentos, Uwe Zönn and Ulena Navarro: Design Decisions for Architecting Digital Twins of Microservices-based Systems	(11:30 - 11:45) Chiharu Naiki, Naoki Tachibana and Nozomi Yamamoto, Musculoskeletal Model Analysis for Underwater Assist Suit for Elbow Joint	(11:30 - 11:45) Johanna Sepúlveda, Dominik Marchbreiter and Filipo Maria Cardano, Hybrid Quantum-secure Group-based Communication with PQC and QKD	(11:30 - 11:45) Bhavya Anura, Paul Gottschalt, Ariel Podlunne, Sergio Pertuz and Diana Goehring. FPGA-Based Hardware/Software Co-Design for Real-Time Visual SLAM Front-End and Back-End Acceleration
	(11:45 - 12:00) FULL: Muhammad Luq, Nauman Bie Ali, Jürgen Bötterdier and Emelie Engström. What do we know about software analytics research? A critical review of secondary studies	(12:00 - 12:20) FULL: Cong Wang, Ali Torbati and Verena Kötts. Exploring Explainability Requirements for Self-Adaptive Systems	(11:45 - 12:00) WIP: Luis Ribeiro, Tiago Costa, Ricardo Severino and Luis Ferreira. Towards the evaluation of the Arrowhead SoA in ITS	(11:45 - 12:00) Fatemeh Alkbarani, Robbe Vlaeminck, Joran Verheijen, Amin Aminifar and Amir Aminifar. Federated Learning for Obstacle Detection to Assist the Visually-Impaired Using Augmented Reality	(11:45 - 12:00) Michael Kühr, Maximilian Mittmann, Mohammad Hamed and Sebastian Steinhilber. CATI - An Open-Source Framework to Evaluate Attacks on Cameras for Autonomous Vehicles	(11:45 - 12:00) Alberto Sciotto, Paolo Savio, Francesco Lubrano, Federico Sirano, Antonino Nespoli, Olivier Terzo, Corrado De Sio and Luca Sterpone. Enabling Time-Aware Priority Traffic Management over Distributed FPGA Nodes
	(12:00 - 12:15) SHORT: Maria Fernanda de Abreu Aguiar, Erica Ferreira de Souza, Katia Romero Felizardo and Luciana Rebelo. Computational solutions for supporting systematic reviews in software engineering: a comprehensive overview	(12:20 - 12:30) WIP: Hossein Rahmani, Kristof Meisner and Stefan Biffl. Towards Trustworthy Adaptation of Cyber-Physical Production Systems with Contract-Based Design	(12:00 - 12:15) WIP: Vadim Peczyński. Patterns in Design of Microservices Architecture: IT Practitioners' Perspective	(12:00 - 12:15) Andrea Vitzthüme, Giovanni Nicolini and Giuseppe Scotti. A Multi-Channel Threshold-Based Seizure Detection Algorithm for Low-Complexity Hardware Implementation	(12:00 - 12:07) (WIP 7 min) - Zafar Atsal, Matthias Ernst, Ibai Ingoien Ceberto, Gaspar Skvarc Botic, Albrecht Mayer, Thomas Wild and Andreas Herkenendorf. An Approach for Automotive ECU Diagnosis via Ethernet Snooping & Microcontroller Tracing	(12:00 - 12:15) Lu Jiang, Zuwen Ou and Diana Goehring. Hardware-Level Adaptive Scheduling for Reconfigurable Accelerators on Virtualized FPGAs
				(12:15 - 12:23) (WIP 7 min) - Claire Béranger, Alexandre Bordat, Petr Dobias, Ngoc-Son Vu, Julien Le Kermec, David Guyard and Olivier Romain. Human Activity Recognition Using SVM-based on micro-Doppler Radar Data Classification	(12:07 - 12:14) (WIP 7 min) - David Andreu, Beatriz Otero and Ramon Castel. Multi-Hardware-Attack Dataset and ML-based Detection Using Processor Stress Patterns on x86	(12:15 - 12:22) (WIP 7 min) - Yen Ying Ng. Eye-Tracking Analysis of Instructor Presence Effects on Educational Video Learning: Extended Abstract
12:30 - 14:00	Lunch Break - Grand Hotel Rooftop					
	Session: MOEML 1 Session chair: Alessio Bucciotti/Robbert Jongeling Room: Sala Furure	Session CPS 2 Session chair: Verena Kötts Room: Sala Ischia		Session DSD 6-1: European and Major Multi-Partner Projects in Digital Systems Design (EMPSD13) Session Chair: Emanuele Torti Room: Sala Maiori	Session DSD 6-2: Formal Methods, Secure Computation and AI-based Vulnerability Repair Session Chair: Javier Fernández Room: Sala Amalfi	Session DSD 6-3: Energy-Aware AI Architectures and Optimization for Embedded Systems Session Chair: Mateusz Wasala Room: Sala Positano
14:00 - 15:30	(14:00 - 14:15) FULL - Philipp Zech, Christopher Keller, Sascha Hammes, Judith Michael and Ruth Irene. Meta-metamodel-Independent Model Transformations	(14:00 - 14:20) FULL: Yelyzaveta Kurkchi and Catia Trubiani. Ethics-Based Requirements for Engineering Cyber-Physical Systems: a Literature Study		(14:00 - 14:15) Radovan Stojinovic and Jovan Djurkovic, Medical Data Over Sound for Vital Signs Monitoring	(14:00 - 14:15) Mohammad Fakhri, Rahul Dharmaji, Halima Bouzidi, Gustavo Araya, Oluwatomi Ogunbare, Mst Ayinsha Siddika and Mohammad Al Faruque. LLM4CVE: Enabling Iterative Automated Vulnerability Repair with Large Language Models	(14:00 - 14:15) Matthieu Rodet, Jean-Luc Béchennec, Mikael Briday, Sébastien Faucois, Isabelle Puaud and Evren Rohou. Circadia: Checkpointing for Intermittent Computing in AI Driven Applications
	(14:15 - 14:30) FULL - Vladyslav Bulhakov, Giordano D'Aleio, Claudio Di Sipio, Antimissa Di Marco and Davide Di Ruscio. Investigating the Role of LLMs Hyperparameter Tuning and Prompt Engineering to Support Domain Modeling	(14:20 - 14:40) FULL: Ulrike Engels and Sibylle Schupp. Configurable abstraction of signals using signal temporal logic	(14:15 - 14:30) Bruna Maria Vittoria Guerra, Roberto Soldi, Lorenzo Vecchi, Stefania Sozzi, Leo Russo, Micaela Schmid and Stefano Ramat, FIRM4MedRob: A Next-Generation Platform for Intelligent Robotic Rehabilitation	(14:15 - 14:30) Jan Keinkathöfer and Rolf Drechsler, Automatic Polynomial Formal Verification of a Floating Point Multiplier	(14:15 - 14:30) Roozbeh Siyadatkhah, Fatemeh Mehrfarooz, Nele Mertens and Todor Stefanov, Transformer-Based Method for Weight Extraction from AIMC Tiles	
	(14:30 - 14:45) FULL - Adem Ali, Javier Luis Canovas Izquierdo and Jordi Cabot. Towards Modeling Human-Agentic Collaborative Workflows: A BPMN Extension	(14:40 - 15:00) FULL: Tiziano Munero, Matteo Turajski, Simon Baner and Marko Halak. A Systematic Approach to Fault Injection Test Case Generation in Practice	(14:30 - 14:45) Luciano Ricci, Christian Celdolani, Umberto Nuzzi, Massimo Biagini, Stefano Cherubini, Adamjan Dikpal, Tor Andre Haugdal, Andrea Aliverti, Alessandra Angelucci, Giovanni Agosta, Gerardo Pelosi, Paolo Bellicio, Samuele Polistina, Riccardo Volpi, Luigi Malagù, Michael Schneider, Florian Wlocek and Vahid Eghlilar. The CAPSAIR Approach to Cyber-Secure Wearable, Ultra-Low-Power Networked Sensors for Soldier Health Monitoring	(14:30 - 14:45) Antonio Rai, Antoine Leloutec, Mikael Carmona, Simon Ponté, Guénél Renaud, Benjamin Smith and Emanuele Valera, Optimizing HDC using Frobenius-Additive FFT on a RISC-V based System-on-Chip	(14:30 - 14:45) Rhythik P and Deepak Gangadharan, RL-ALMS: Reinforcement Learning based Adaptive and Lightweight Model Selection Framework for Energy-Efficient Lane Detection in Electric Vehicles	
	(14:45 - 15:00) FULL - Robbert Jongeling, Neel Jagers Strom, Lars Peter Torg Nissen, Martin Kitchen and Jan Carlson. Adopting the C4 model for lightweight architecture modeling - An experience report	(15:00 - 15:20) FULL: Beatriz Cabrero-Daniel and Mazen Mohammad. Challenges of Virtual Validation and Verification for Automotive Functions			(14:45 - 15:00) Ran Huo and Jose Nunez-Yanez, FPGA-Accelerated Early-Exit Neural Decoder for Quantum Error Correction	(14:45 - 15:00) Patrick Schmidt, Fabian Kröll, Alexey Serdyuk, Matthias Stammler, Tanja Harbaum and Juergen Becker. DSEParted: Co-Optimization of Embedded NPU Architectures and Neural Network Partitioning
	(15:00 - 15:15) SHORT - Giacomo Garaccone, Riccardo Coppola and Luca Ardito. An Automated Diagram Generator of Reference Solutions for Modeling Educators				(15:00 - 15:07) (WIP 7 min) - Pedro Lisa, Stefano Losa, Karim Costa, Daniel Araújo, Aluizio Rocha Neto, Nello Castro, Thais Batista, Everton Cavalcante, Frederico Lopes and Eduardo Nogueira. Enhancing Public Safety Situational Awareness Using Edge Intelligence	(15:00 - 15:07) (WIP 7 min) - Louis Ledoux, Pierre Cochard and Florent de Drechin. Towards Optimized Arithmetic Circuits with MLIR
15:30 - 16:00	CLOSING Room: Sala Vietri					